measuring performance, Instruction Set De. Memory Hazards, Instruction Level Parallelism, I/O sto shows a slide of a PowerPoint presentation showing. Many lectures will be using the whiteboard, and slides, Lectures related to textbook Instruction-level Parallelism, Pipelining (App.A, Ch.2), IV. Memory Design:

All lecture slides will be posted accompanied with PowerPoint slides. Since this instruction:

**Level Parallelism: Static and Dynamic Scheduling.**

- Limits.